

# SOLID-STATE DISK

Practically all of today's PCs have a hard disk and one or more floppy disk drives. These 'media' allow the computer to load the files it needs to run a certain program. Unfortunately, there are circumstances in which the use of these magnetic/mechanical media is problematic. In cases where shock, vibration, large temperature variations or an otherwise electrically hostile environment force you to turn away from the 'usual' disk drives, the solid-state disk discussed in this article is a very good alternative.

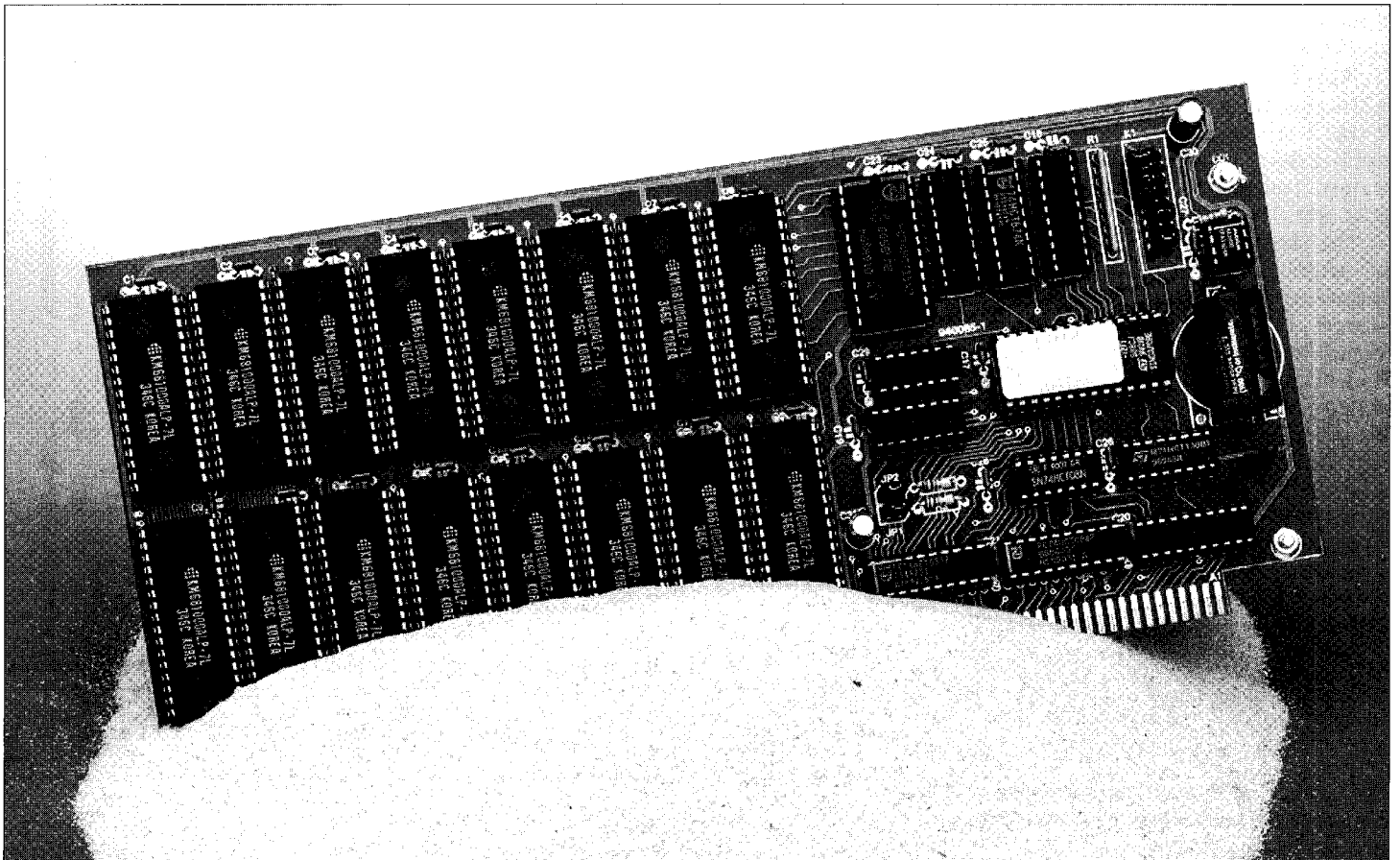
Design by B. Yahya

ALTHOUGH much larger media are pressing at the gates, the floppy disk is still the most popular portable information carrier. A DOS formatted 3.5-inch floppy disk has a capacity of 1.44 MByte, which is, in principle, enough for the MS-DOS operating system and one or two small application programs. The main disadvantages of the floppy disk are the relatively low data transfer speed, and the risk of data corruption. Also, a diskette is

easily lost or stolen. Like the drive unit of a hard disk, the floppy disk drive is a mechanical system with read/write heads and motors. Unfortunately, mechanical parts are subject to wear and tear and eventual breakdown. Furthermore, additional safety precautions have to be taken where a floppy disk drive is used in environments with a high explosion risk. The danger is caused mainly by the tiny sparks which occur in the drive motor.

## MAIN SPECIFICATIONS

<b>Application:</b>	in any MS-DOS PC
<b>Capacity:</b>	max. 2 MByte
<b>Disk emulation:</b>	1.44 or 2.88 MByte
<b>Memory type:</b>	static
<b>Data buffer:</b>	Lithium battery
<b>I/O addresses:</b>	300H, 308H, 310H or 318H
<b>BIOS:</b>	in EPROM
<b>Software:</b>	not required
<b>Hardware:</b>	8-bit insertion card



The solid-state version of the floppy disk drive is a plug-in board containing static RAMs and memory backup battery. Arguably, it is totally free from the above disadvantages. It is quiet, fast and easily protected against corruption of data. Also, being secured inside the PC, the solid-state disk is not so easily lost or stolen.

The solid-state disk may be configured such that it can be used to 'boot' the computer, i.e., it acts as a disk from which the PC loads its start-up software. With the correct software installed, the PC starts automatically from the solid-state disk after every reset. A write protect switch on the board makes unauthorized modification of the data contained in RAM far from easy. That also adds to the reliability of the PC, enabling it to log in straight away on a network. Another application where a solid-state disk has the edge over the more traditional magneto-mechanical media is remote logging, for instance, in an automated weather or air quality monitoring system.

### Circuit description

The structure of the solid-state disk is relatively simple. Static memory devices are used to create a RAM bank of 2 MByte. A section of 1.44 MByte is allocated to the RAM disk function, the rest is freely available. In principle, it is even possible to use the full 2 MByte simply by configuring the solid-state disk as a diskette with a capacity of 2.88 MByte. This works as long as you, the user, ensure that no more than 2 MByte is written to the disk. Although an error report is generated if the 2-MByte limit is exceeded, many application programs will unfortunately prove unable to handle this properly.

The circuit also contains an EPROM in which all relevant BIOS (basic input/output system) information is stored. These BIOS routines serve to simulate a mechanical diskette station. The solid-state disk is therefore called just like an 'ordinary' disk drive by interrupt routine 13H. That interrupt has three basic functions: reading a sector (02); writing a sector (03); and formatting a sector (05). Those BIOS routines that have no relevance for the solid-state disk contain only a return instruction reporting 'OK' (C=0; AH=0). This enables the computer to actually

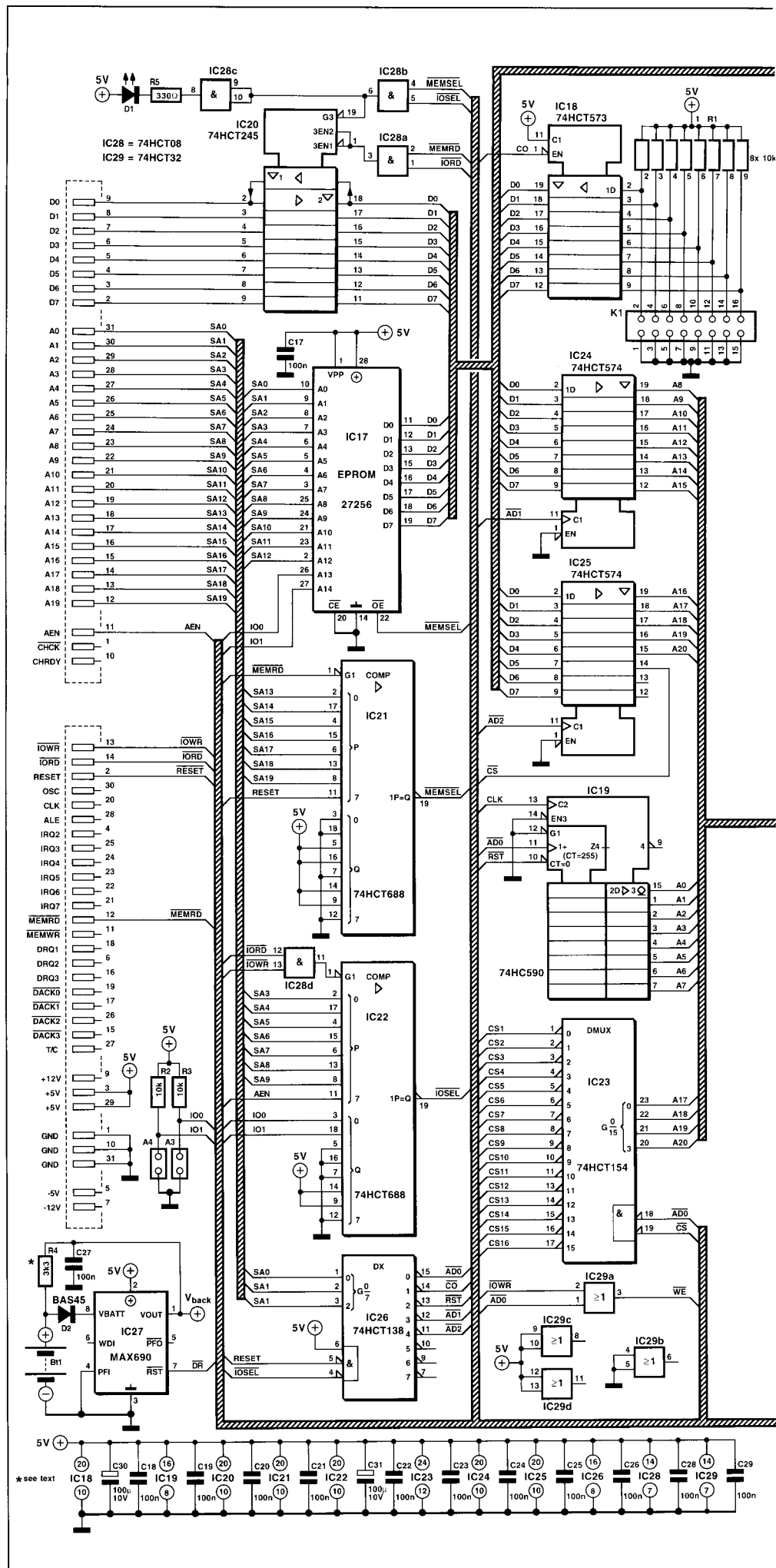
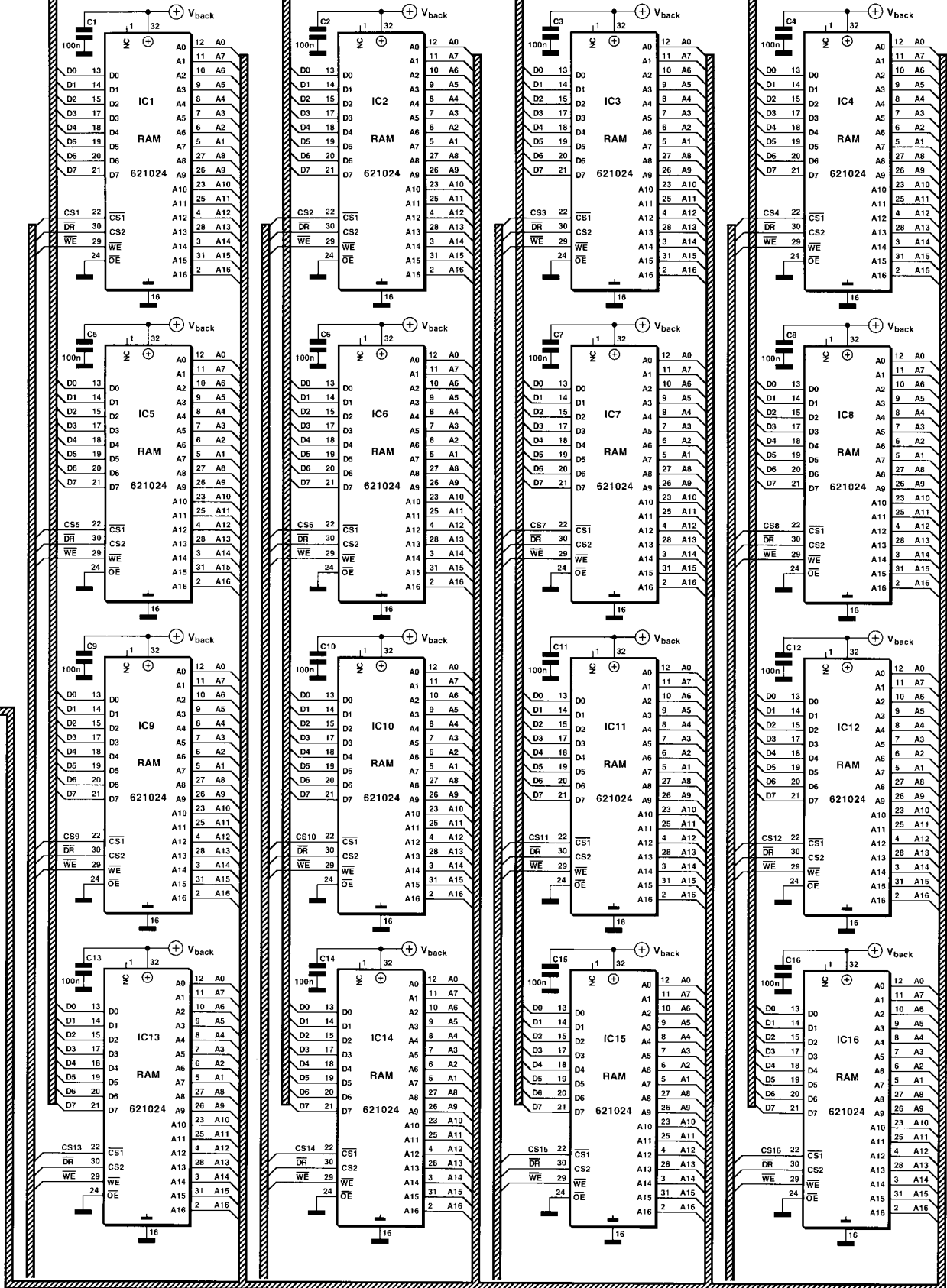


Fig. 1. Circuit diagram of the solid-state disk card. Note the huge memory bank consisting of static RAMs.



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'boot' from the solid-state disk.

The circuit diagram of the solid-state disk is given in Fig. 1. The striking element in the drawing is, of course, the RAM bank which consists of 16 ICs with a capacity of 128-KByte each.

With the exception of the BIOS ROM, the entire circuit is addressed in the I/O range. The base address of the card may be set to 300H, 308H, 310H or 318H with the aid of jumpers. In addition to the space occupied by the BIOS ROM, the card uses five memory locations, namely (base) through (base+4). The BIOS ROM is addressed in the memory block between D8000H through D9FFFH.

Nine ICs are used to couple to card to the PC's extension bus. Three of these, IC<sub>21</sub>, IC<sub>22</sub> and IC<sub>26</sub>, perform the address selection. IC<sub>21</sub> and IC<sub>22</sub> are comparators type 74HCT688. The databus is buffered by IC<sub>20</sub>, a 74HCT245. The BIOS ROM address is selected by IC<sub>21</sub>, while IC<sub>22</sub> and IC<sub>26</sub> fix the I/O address assigned to the card.

Some tricks are in order to be able to read the memory bank whilst emulating a mechanical (floppy) disk drive. A floppy disk is usually partitioned in sectors of 256 bytes each (each of the 80 tracks on a 3.5-inch diskette has either 18 or 36 sectors). These 256 bytes are read in succession by issuing an equal number of read pulses. This system is mimicked on the solid-state disk. The 256 read pulses are used to enable IC<sub>19</sub>, a synchronous 8-bit counter, to drive the lower-order eight address lines. The higher-order address lines are supplied by IC<sub>24</sub> and IC<sub>25</sub>.

Circuit IC<sub>18</sub> has a key function in the circuit, because it reads the eight configuration bits set by the user with the aid of jumpers. Note, however, that the software set-up uses only four of these eight bits.

To address a memory location on the solid-state disk, the desired address is copied into two latches, IC<sub>24</sub> and IC<sub>25</sub>. Addresses (base+3) and (base+4) are available for that purpose.

Base:	read/write to/from RAM memory; increment sector counter
Base+1:	read configuration bits
Base+2:	reset sector counter
Base+3:	set A8-A15
Base+4:	set A16-A20 and CS

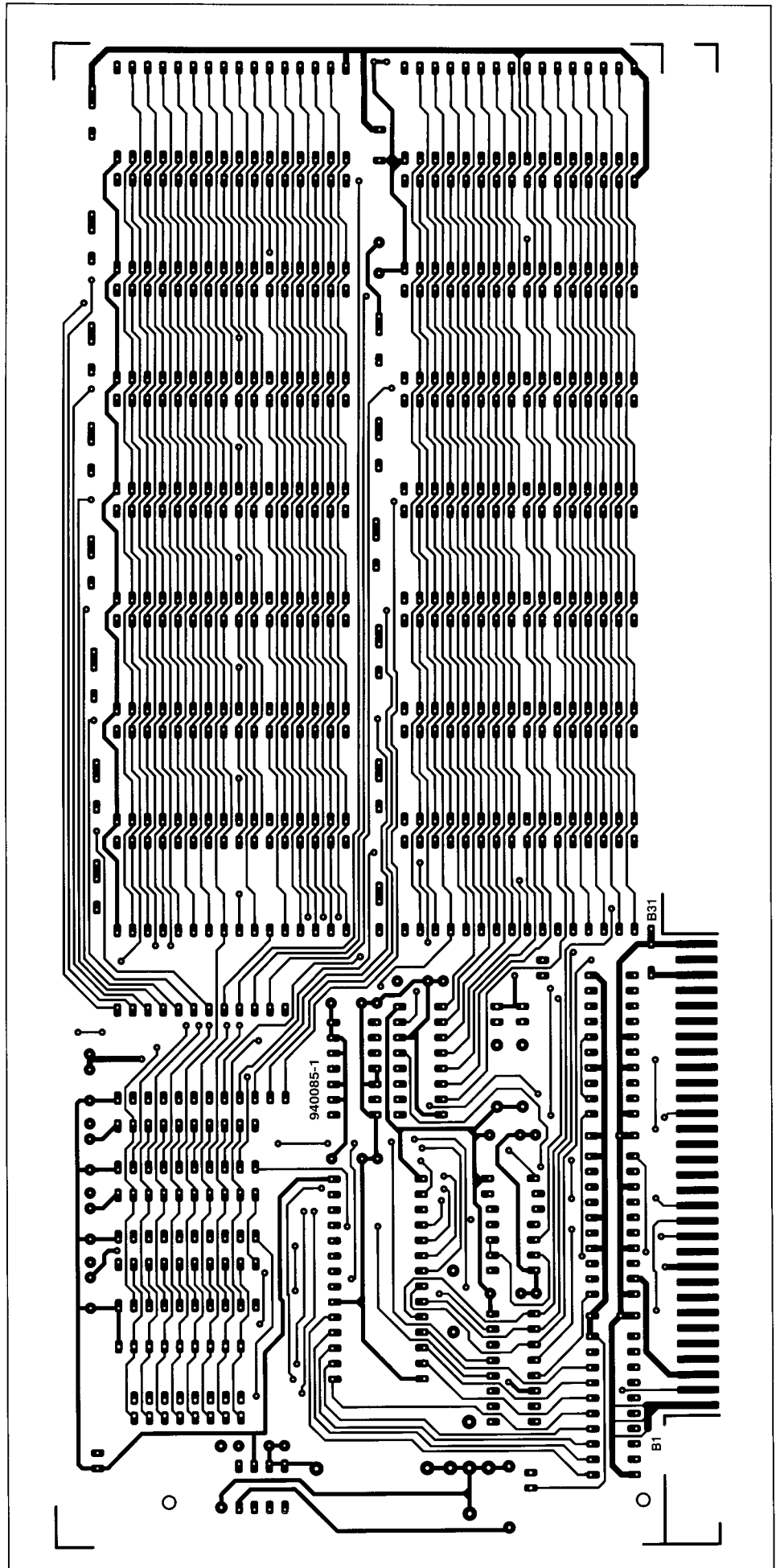
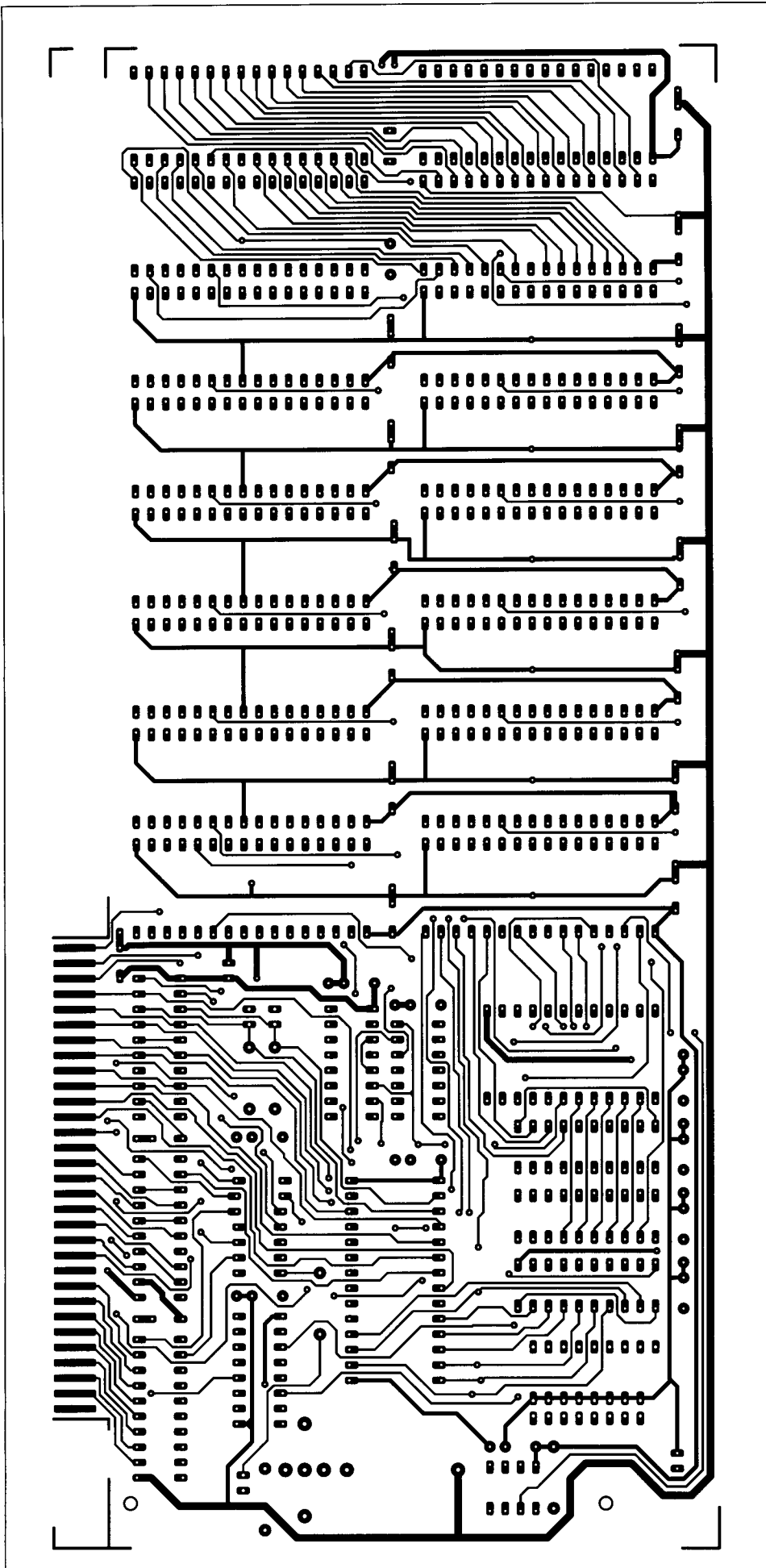


Table 1. Solid-state disk card addresses.

Fig. 2a. Track layouts of the double-sided, through-plated printed circuit board. This board is



Writing to address (base+2) causes 8-bit counter IC<sub>19</sub> to be reset. As soon as the address is copied into the latches via the PC's I/O connection, the information from the selected sector can be requested by sending out a series of read pulses.

The individual memory components are selected via IC<sub>23</sub>, a type 74HCT154. This IC generates a selection signal on the basis of the highest address lines of the memory chips.

An important function is reserved for IC<sub>27</sub>, a type MAX690. This 'watchdog' IC continuously monitors the supply voltage, and ensures that the write protection is actuated via the DR signal when the supply voltage drops below a certain minimum level. The write protection then prevents the contents of the memory being changed. Next, battery Bt<sub>1</sub> takes over the supply of the memory via diode D<sub>2</sub>. The specified battery guarantees a safe storage period of three years. The watchdog IC also ensures that all memory components are enabled again when the supply voltage is restored. Resistor R<sub>4</sub> has a function if a NiCd (Nickel-Cadmium) battery is used for Bt<sub>1</sub>. It is omitted if a Lithium battery is used.

Finally, LED D<sub>1</sub> has the same function as the light on the front panel of any diskette station. The LED lights whenever the solid-state disk is selected, indicating that the computer is busy with a read or write operation on the RAM disk. If you use a high-efficiency LED in this position, you are sure not to miss even the shortest read/write pulses.

## Construction

Although the circuit looks quite sizeable, construction is by no means a long-winded affair. Anyone capable of soldering accurately should be able to produce a working copy of the card. The track layouts and component mounting plan of the printed circuit board are shown in **Fig. 2**. Since the board is double-sided and through-plated, it is not recommended to make it yourself. Moreover, to prevent serious problems, the integrated connector should fit very accurately into the PC's extension bus slot. The ready-made board supplied through our Readers Services comes with gold-plated contact fingers for the best possible connection to the extension slot contacts.

In view of the cost of the memory components on the board it is recommended to use IC sockets for the RAMs and the BIOS EPROM. Start the construction by fitting these sockets. Next, do the passive parts, taking good care to observe the polarity of the electrolytic capacitors. The ICs are fitted

## COMPONENTS LIST

### Resistors:

$R_1$  = 8-way 10k $\Omega$  SIL resistor-array  
 $R_2, R_3$  = 10k $\Omega$   
 $R_4$  = 3k $\Omega$   
 $R_5$  = 330 $\Omega$

### Capacitors:

$C_1$ - $C_{29}$  = 100nF  
 $C_{30}, C_{31}$  = 100 $\mu$ F/10 V

### Semiconductors:

$D_1$  = high-efficiency-LED, dia 3 mm  
 $D_2$  = BAS45  
 $IC_1$ - $IC_{16}$  = 621024 or KM681000  
 (1 Mbit static RAM)  
 $IC_{17}$  = 27256 (order code 946641-1)  
 $IC_{18}$  = 74HCT573  
 $IC_{19}$  = 74HC590  
 $IC_{20}$  = 74HCT245  
 $IC_{21}, IC_{22}$  = 74HCT688  
 $IC_{23}$  = 74HCT154  
 $IC_{24}, IC_{25}$  = 74HCT574  
 $IC_{26}$  = 74HCT138  
 $IC_{27}$  = MAX690  
 $IC_{28}$  = 74HCT08  
 $IC_{29}$  = 74HCT32

### Miscellaneous:

$K_1$  = double-row 16-pin header.  
 Jumpers as required.  
 $Bt_1$  = ER 1/2 AA (Varta).  
 1 cover bracket type KHPC L22833  
 (Eurodis Texim).  
 Printed circuit board plus BIOS  
 EPROM: order code 940085 (see page  
 70).

last. Here, too, mind the polarity, because errors are easier made than found, paid for and corrected (in that order). Next, check for errors in your soldering work. If you are convinced that everything is in order, the mounting bracket may be fitted on to the PCB.

## Setting up

The functions of the jumper options offered by  $K_1$  are listed in **Table 2**. The default base address of the card (no jumpers fitted) is 318H. That address range is reserved for experimental circuits, and is usually free. If 318H is already occupied by another extension card in your system, relocate the solid-state disk to an alternative base address using the A3 and A4 jumpers. Alternatives are indicated on the component overlay printed on the circuit

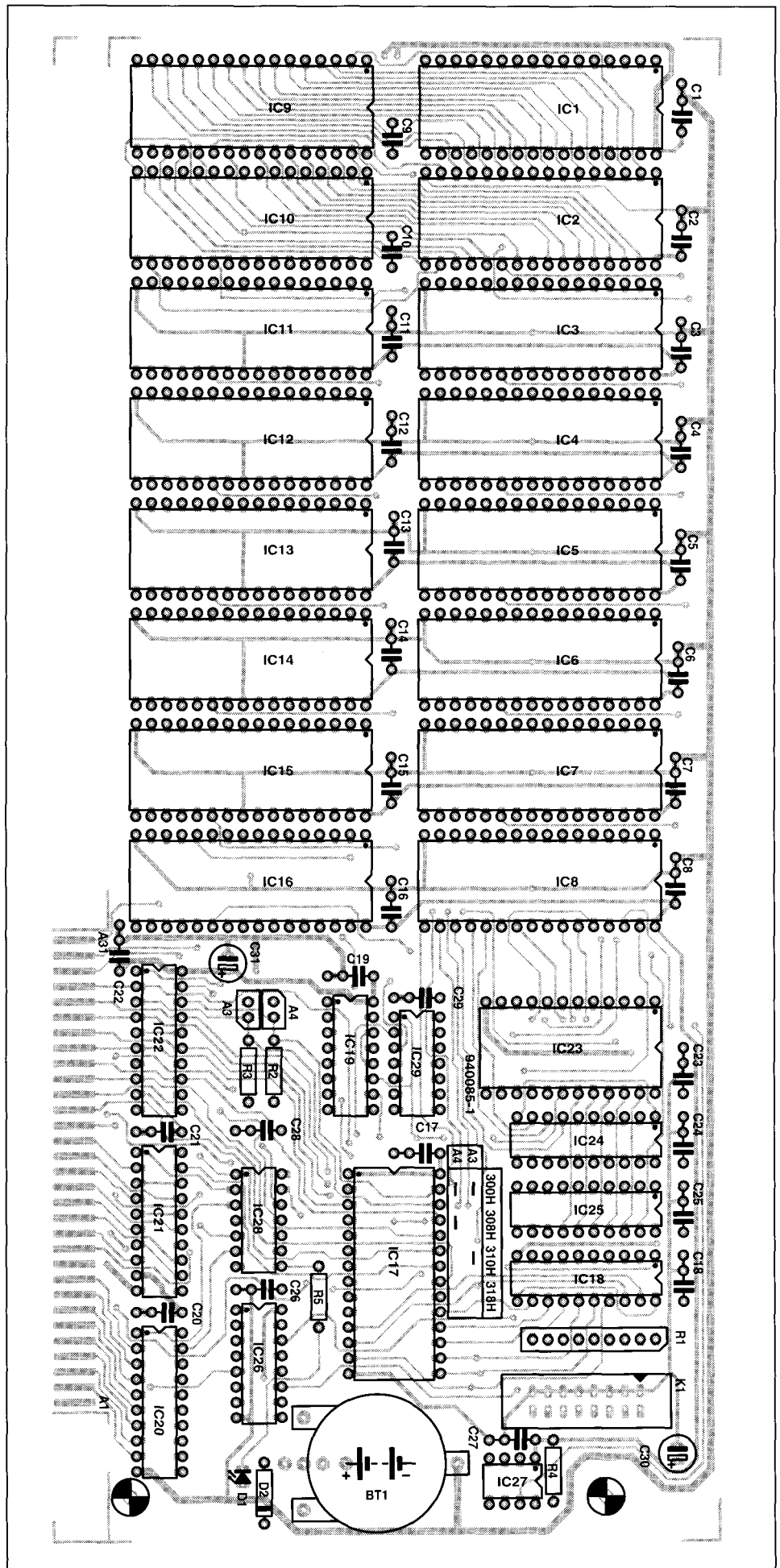


Fig. 2b. Component mounting plan.

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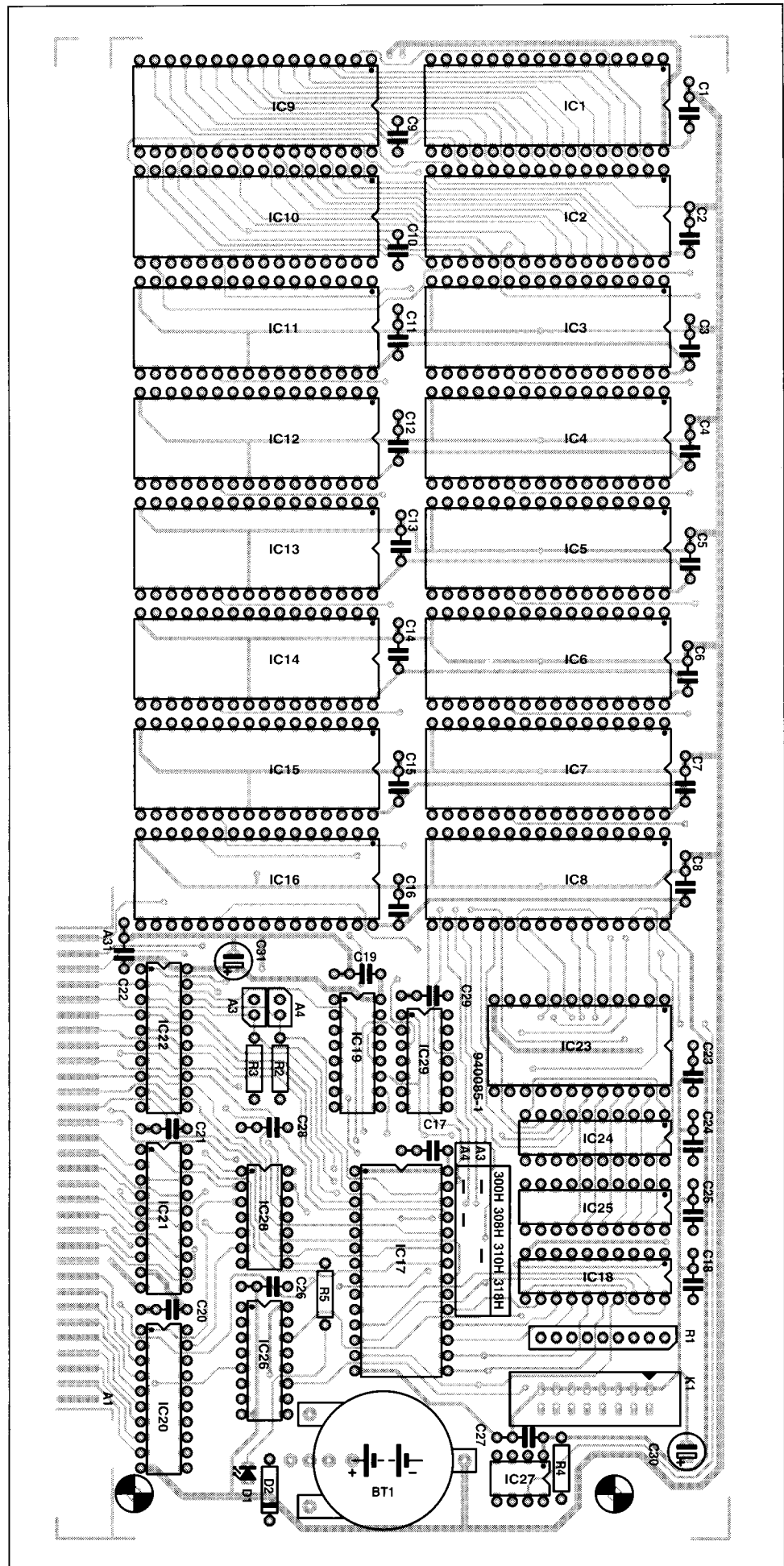


Fig. 2b. Component mounting plan.